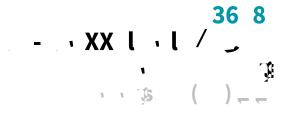


A



P ,

NSD3608 is a highly integrated device which includes eight half bridge (HB1~HB8) gate drivers and two current sense amplifiers.

Gate drivers provide advanced functions like slew rate control, switching timing feedback and VGS handshaking. Current sense amplifiers support high common mode voltage input. A 16-bit SPI is used to configure and control device also read out status registers for diagnostic.

Device offers an array of diagnostic features to ensure robust operation. These features include supply voltage monitor, charge pump voltage monitor, VDS overvoltage monitor, VGS voltage monitor and thermal monitor (warning and shutdown protection).

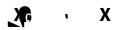


- AEC-Q100Grade1 qualified
- 8halfbridge gate drivers
 - 4.9V to 37V operating range
 - Halfbridge, H bridge and SPI control mode
 - 4 PWM inputs with output mapping
 - Configurable freewheeling mode during PWM
- Configurable charge/discharge current profile driver (CCPD) for improving EMC performance
 - Three phases for MOSFET turn on/off (pre charge/discharge, charge/discharge, post charge/discharge)
 - 0.25mA to 64mA source/sink current for different phases
 - Turn on/off delay and rise/fall timing feedback

- Integrated 2-stage charge-pump with spread spectrum
 - Support 0% to 100% PWMoperation
 - Reverse protection MOSFET driver output
- 2 wide common mode current sense amplifier (CSA)
 - Support high side, low side and inline topology
 - Configurable gain (10/20/40/80V/V)
 - Configurable over current threshold, filter time and fault reaction
- Lowcurrent consumption in sleep mode
- 16-bit, max 10MHz SPI interface
- Protection and diagnostic
 - Supply and regulator voltage monitor (DVDD UV, PVDD OV, PVDD UV and VCP UV)
 - Gate driver monitor (VGS Fault and VDS OV)
 - Support off state diagnostic
 - Thermal warning and shutdown
 - Window watchdog timer
 - Support brake function (LS5~LS8) in sleep mode and normal mode
 - Dedicated driver disable pin (DRVOFF) or Fault interrupt pin (nFLT)
- RoHS & REACH Compliance

A l X 3s

- Automotive brushed DC motor applications (Seat, Power lift gate...)
- Automotive body control functions (Door locks, Latch...)



Pι	b	Pı kı	1 -
NSD3608-Q1QAJR		VQFN56	8.0mm ×8.0mm

36 8

For full version datasheet, please contact: pad_marketing@novosns.com



The information given in this document shall in no event be regarded as a guarantee of any warranty or authorization, express or implied, including but not limited to merchantability, fitness for a particular purpose or infringement of any third party's intellectual property rights.

You are solely responsible for your use of Novosense' products and applications. You shall comply with all laws, regulations and requirements related to Novosense's products and applications, although information or support related to any application may still be provided by Novosense.

The resources are intended only for skilled developers designing with Novosense' products. Novosense reserves the rights to make corrections, modifications, enhancements, improvements or other changes to the products and services provided. Novosense authorizes you to use these resources for the development of relevant applications of Novosense's products, other reproduction and display of these recourses is prohibited. Novosense shall not be liable for any claims, damages, costs, losses or liabilities arising out of the use of these resources.

For further information on applications, products and technologies, please contact Novosense (www.novosns.com).

Suzhou Novosense Microelectronics Co., Ltd